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FIG.1

PULSE WIDTH CONTROL CIRCUIT ACCORDING TO PRESENT INVENTION

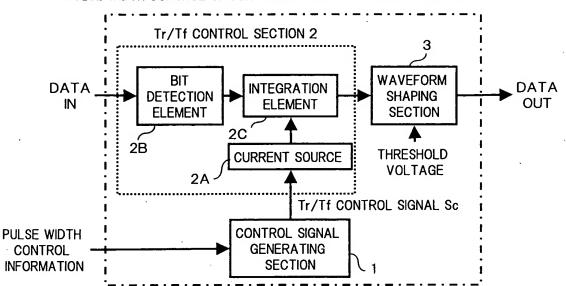


FIG.2

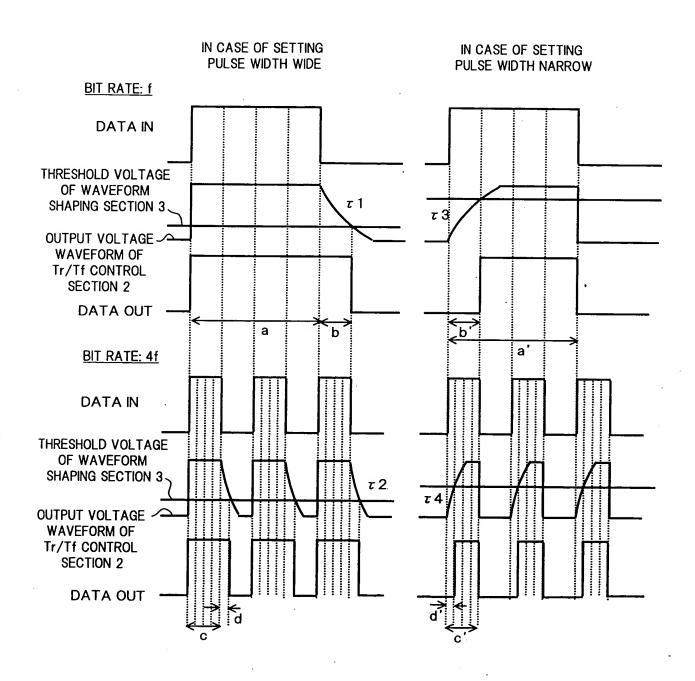


FIG.3

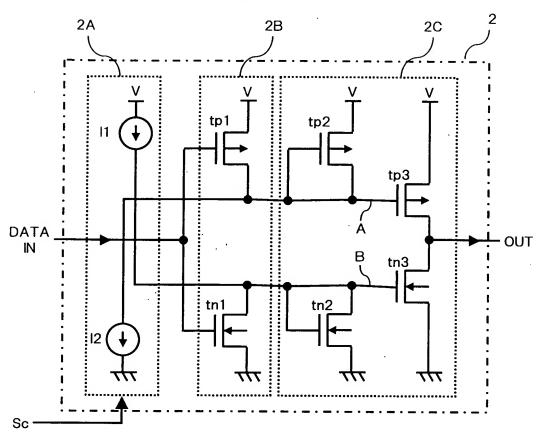


FIG.4

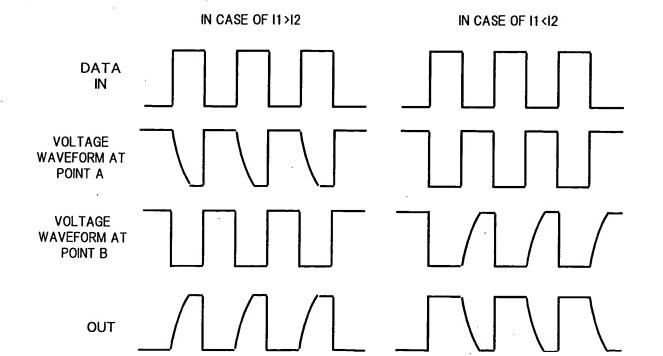


FIG.5

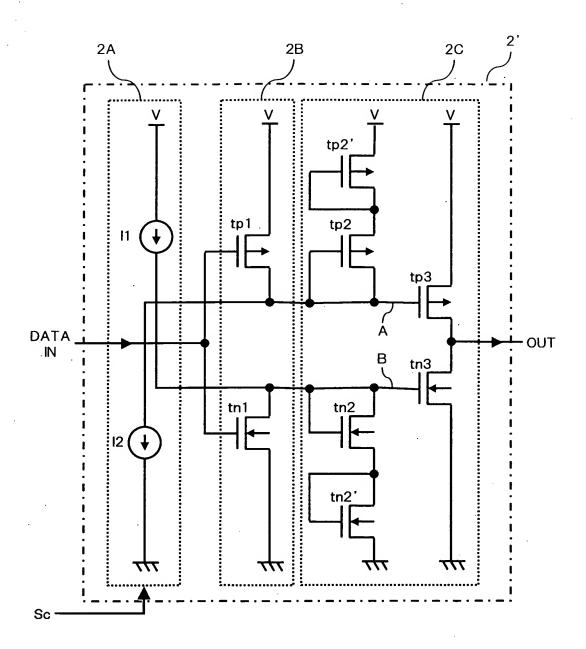
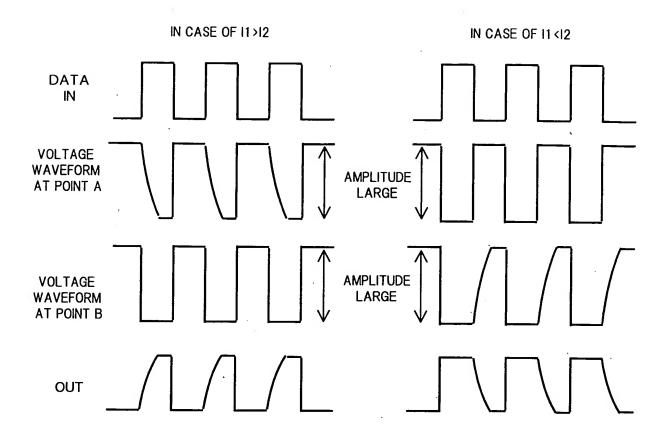
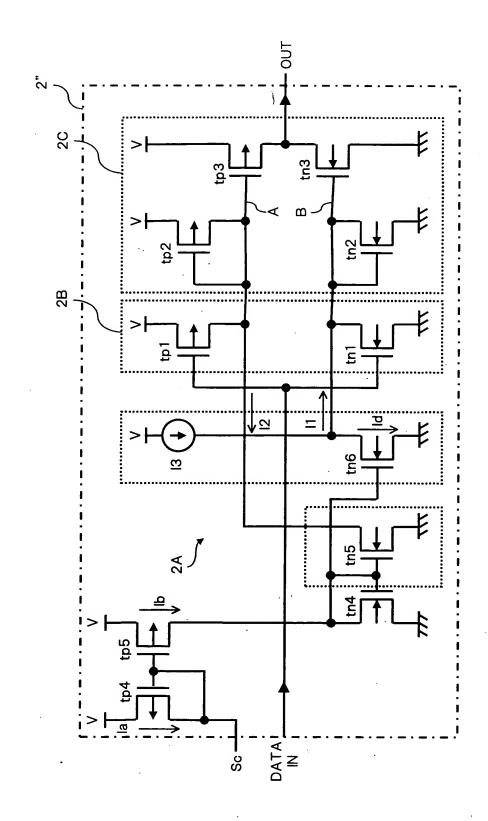


FIG.6





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FIG.8

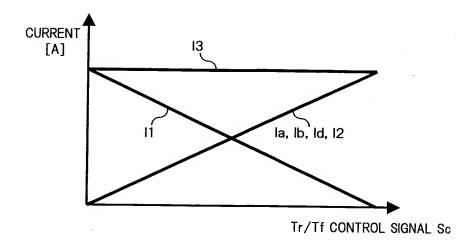


FIG.9

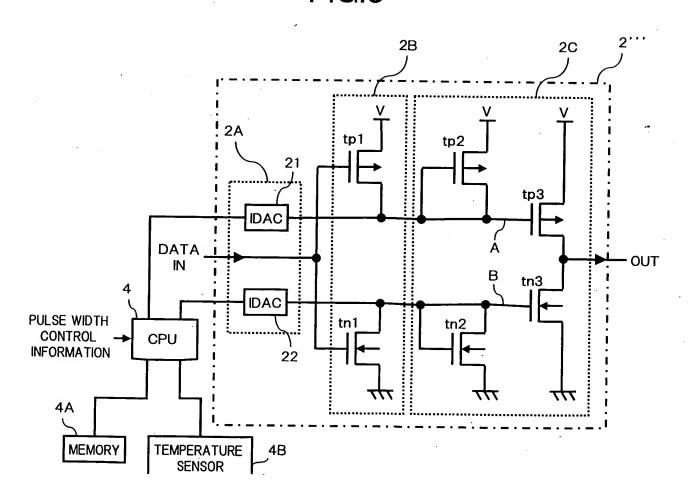
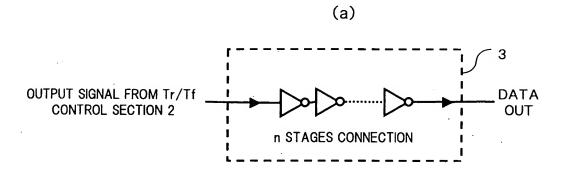
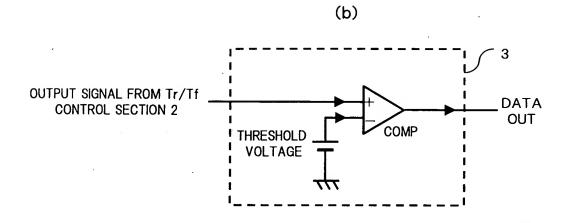


FIG.10





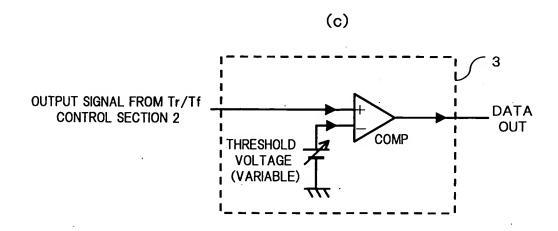
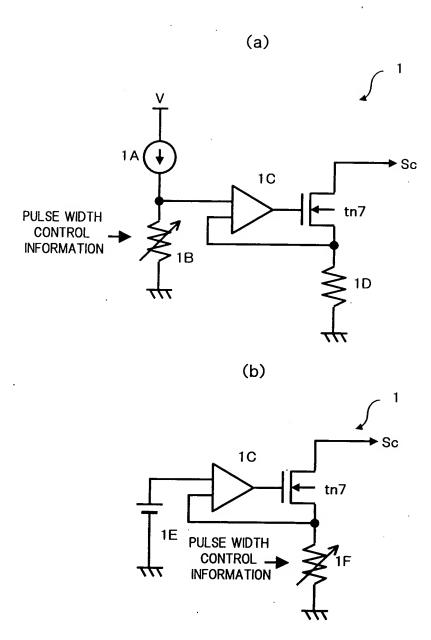


FIG.11



DATA

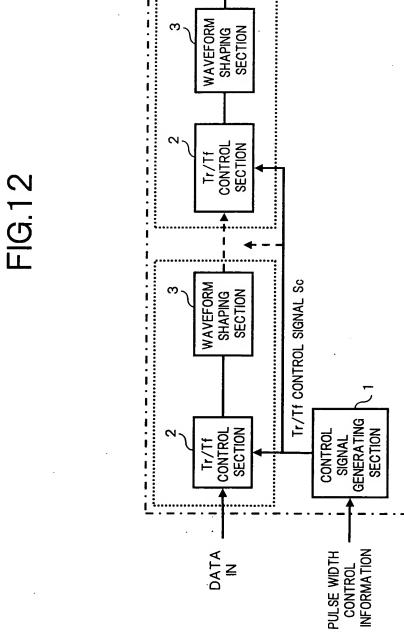


FIG.13 PRIOR ART

CONVENTIONAL PULSE WIDTH CONTROL CIRCUIT

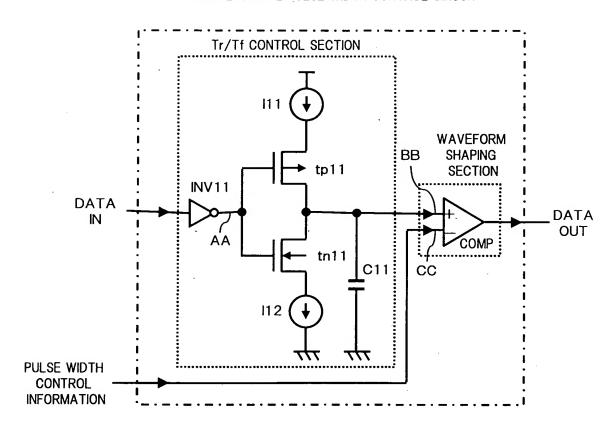


FIG.14 PRIOR ART

OPERATION OF CONVENTIONAL PULSE WIDTH CONTROL CIRCUIT

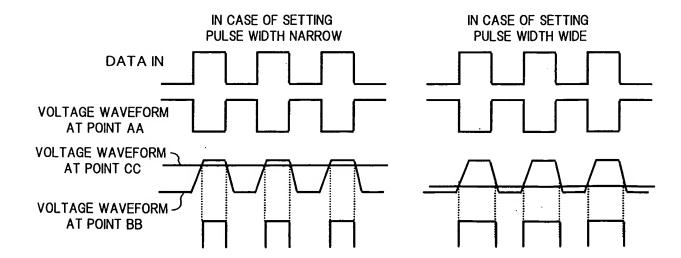


FIG.15 PRIOR ART

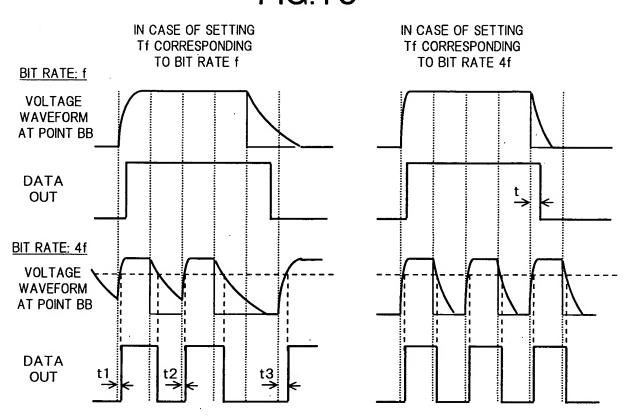


FIG.16 PRIOR ART

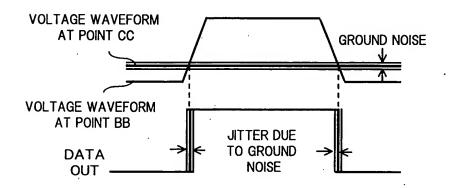


FIG.17 PRIOR ART

